

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

J1040 U.S. PTO
09/971981
10/04/01

In the application of: Igor Y. Khandros et al.	
Application No.:	Examiner: Unknown
Filing Date: October 4, 2001	Group Art Unit: Unknown
For: ELECTRONIC COMPONENT OVERLAPPING DICE OF UNSINGULATED SEMICONDUCTOR WAFER	

GENERAL AUTHORIZATION/PETITION FOR EXTENSION OF TIME

Assistant Commissioner for Patents
Washington, D.C. 20231

Dear Sir:

Throughout prosecution of the above-referenced patent application, and pursuant to 37 C.F.R. § 1.136(a)(3), Applicants hereby authorize and request the Commissioner to treat any correspondence requiring a petition for extension of time as containing a request for the required extension of time. This general authorization is effective during the pendency of the application, including any divisional or continuing application filed there from.

Where no check is received by the Commissioner, the Commissioner is hereby authorized to charge payment of the requisite petition fees, or charge any additional fee

PATENT
Docket No.: P64C1-US
Express Mail No: EL 438390712US

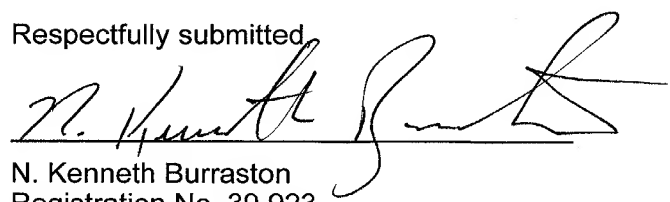
required in connection with correspondence, or credit any overpayment of same, to Deposit

Account No. 50-0285 (order no. P64C1-US).

Respectfully submitted,

Date: October 4, 2001

By:


N. Kenneth Burraston
Registration No. 39,923

FormFactor, Inc.
Legal Department
5666 La Ribera St.
Livermore, CA 94550
925-456-7355
925-294-8147 Fax